

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

Ordering Information

Type NO.	Marking	Package Code
SRA2204EF	4R	SOT-523F

Outline Dimensions

unit : mm

The diagram shows the physical dimensions of the SOT-523F package and its equivalent circuit. The top view shows a rectangular package with dimensions: total width 1.50~1.70 mm, total height 1.50~1.70 mm, and a central width of 0.78~0.98 mm. Pin 1 is on the left, pin 2 is on the right, and pin 3 is at the bottom. The bottom view shows a trapezoidal shape with a top width of 0.63~0.73 mm, a bottom width of 0.10 Max. mm, and a height of 0.16 Max. mm. The equivalent circuit shows a PNP transistor with an input terminal (IN) connected to the base through resistor R₁, an output terminal (OUT) connected to the emitter, and a common terminal (COMMON) connected to the collector through resistor R₂.

• Equivalent Circuit

R ₁	R ₂
47KΩ	47KΩ

PIN Connections
 1. IN
 2. COMMON
 3. OUT

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	V_O	-50	V
Input voltage	V_I	-40, 10	V
Output current	I_O	-100	mA
Power dissipation	P_D	150	mW
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	$V_O=-50V, V_I=0$	-	-	-500	nA
DC current gain	G_I	$V_O=-5V, I_O=-10mA$	80	200	-	-
Output voltage	$V_{O(ON)}$	$I_O=-10mA, I_I=-0.5mA$	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_O=-0.2V, I_O=-5mA$	-	-2.8	-5.0	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_O=-5V, I_O=-0.1mA$	-1.0	-1.2	-	V
Transition frequency	f_T^*	$V_O=-10V, I_O=-5mA, f=1MHz$	-	200	-	MHz
Input current	I_I	$V_I=-5V, I_O=0$	-	-	-0.18	mA
Input resistor (Input to base)	R_1	-	33	47	61	K Ω
Input resistor (Base to common)	R_2	-	33	47	61	K Ω

* : Characteristic of transistor only

Electrical Characteristic Curves

Fig. 1 $I_o - V_{I(ON)}$

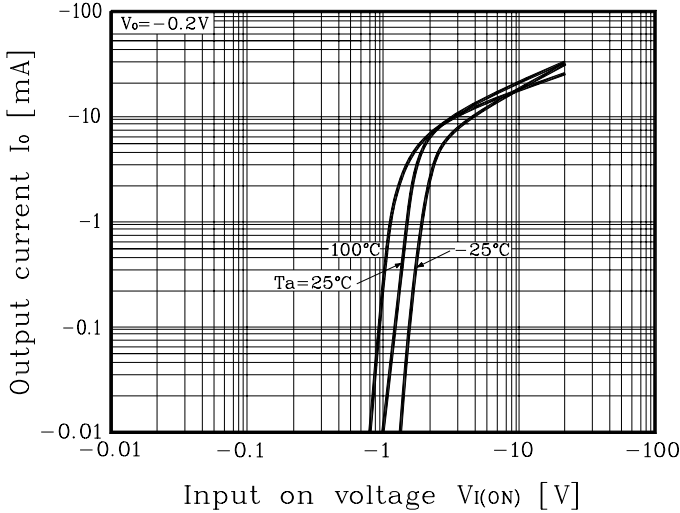


Fig. 2 $I_o - V_{I(OFF)}$

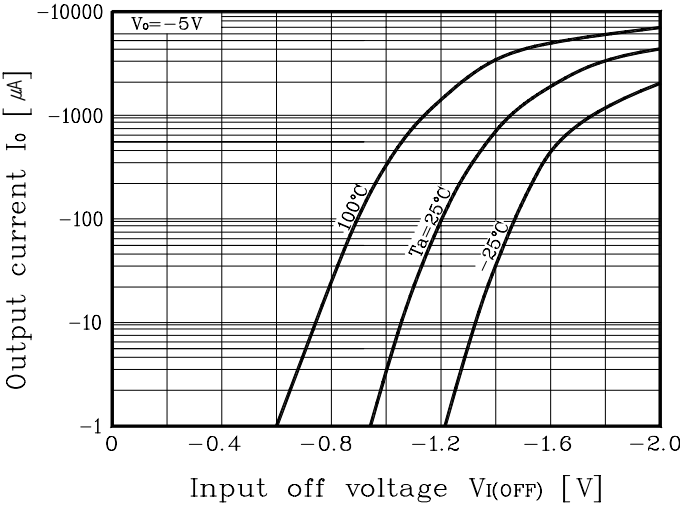
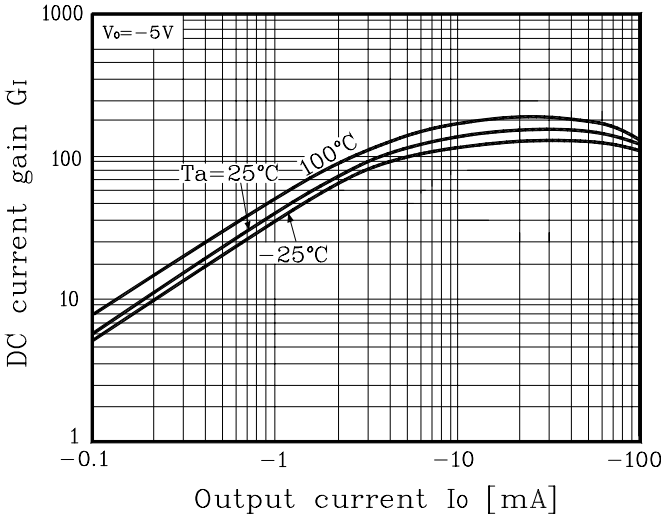


Fig. 3 $G_I - I_o$



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